



Paper #0402

FORM PTO-1449 INFORMATION DISCLOSURE CITATION IN AN APPLICATION (Us several sheets if necessary)	Docket Number (Optional)	Application Number
	YOR9200010249US1	10/052,619
	Applicant	
	Magerlein et al.	
Filing Date	Group Art Unit	
January 18, 2002	Not yet assigned	

U. S. PATENT DOCUMENTS

EXAMINER INITIAL	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
EP	6,339,024	1/15/02	Petrarca et al.	438	686	
EP	6,344,125	2/5/02	Locke et al.	205	118	
EP	6,368,484	4/9/02	Volant et al.	205	220	

FOREIGN PATENT DOCUMENTS

	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	Translation	
						YES	NO

OTHER DOCUMENTS (including Author, Title, Date, Pertinent Pages, Etc.)

EP	Richard Volant, Kevin Petrarca, Peter Locke, James A. Tornello and Donald F. Canaperi, Tittle: "Replated Metal Structures for Semiconductor Devices", U.S. Patent application Serial No. 09/657,469, filed on May 9, 2000.

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